

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	51486	voltage generator	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:45
L2	26077	Vpp or generated voltage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:46
L3	3357	I1 and I2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:46
L6	93	boosting section	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:47
L8	115057	external evaluation device or tester	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:48
L10	21712	expected voltage or Vref	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:48
L11	507	I8 and I10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:48
L12	50	I3 and I11	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:49

L13	172	compar\$3 and (Vpp with Vref)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:49
L14	811	compar\$3 and (reference voltage with generated voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:49
L15	967	l13 or l14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:50
L16	7	l15 and l12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:56
L17	325875	Flash memory or FLASH memory or FLASH	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:59
L18	39436	decision register or data register	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:59
L19	3772	l17 and l18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 14:59
L20	5	l19 and l16	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:00

L21	1620385	(CPU or central processing unit or controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:00
L22	5	I20 and I21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:05
L23	89773	semiconductor integrated circuit\$1	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:06
L24	76733	oscillat\$3 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:07
L25	10104	frequency divid\$3 circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:07
L26	634	load\$3 and (control data with data register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:08
L27	80596	non-volatile memory or non-volatile storage	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:08
L28	447	stor\$3 and (control data with I27)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:08

L29	6	I28 and I26	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:09
L30	1	I29 and I22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:09
L31	1715	I23 and I24	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:09
L32	158	I31 and I25	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:09
L33	8	I32 and I18	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:10
L34	2	I33 and I15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:16
L36	102	(determin\$3 or compar\$3) and (pulse width with reference pulse signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:17
L37	1	I32 and I36	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:20

L38	29476	updat\$3 and (control data)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:21
L39	2	I33 and I38	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:23
L40	35324	testing mode or test mode or test operation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:24
L41	2	I33 and I40	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:25
L42	2	I39 and I32	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:27
L44	10979	714/724 or 365/? or 327/? or 324/? or 714/699 or 714/?	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:29
L46	19	I44 and I3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:54
L47	2	I46 and I15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:31

L48	1	compar\$3 and (pulse width near2 periodic signal with pulse width near2 reference pulse signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:56
L49	1	compar\$3 and (pulse width near2 clock signal with pulse width near2 reference pulse signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/06/11 15:57